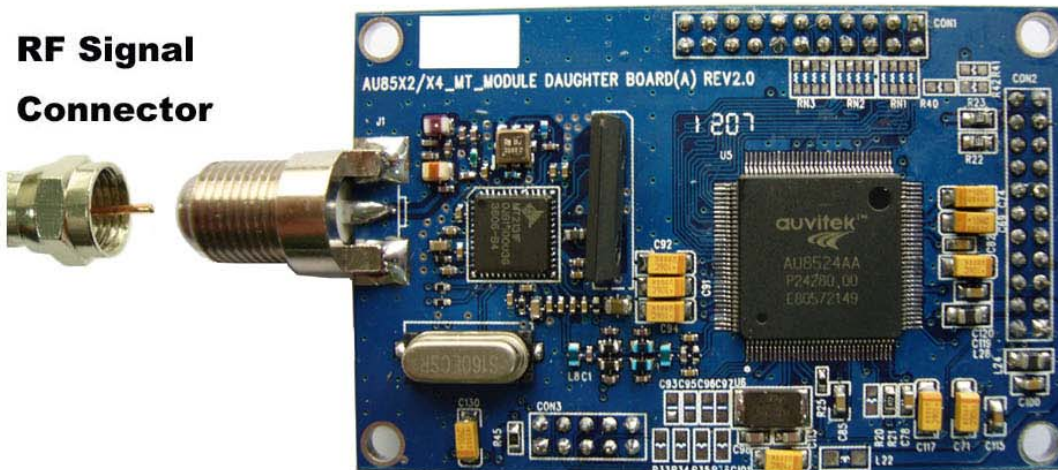




AU8524 MT NIM Module Design Brief

1 Introduction

This reference design is used to demo AU8524AA chip. It supports the ATSC and J83B.



2 Hardware Configuration

2.1 Major chip set

No.	Function	Name/Module	Manufactory	Provider	Remark
1	ATSC/NTSC etc. Decoder	AU8524AA	Auvitek	Auvitek	
2	Tuner	MT2131	Microtune	Microtune	

2.2 Functions List

No.	Function	Details	Remark
1	Tuner Setting	Support MT2131 tuner	
2	ATSC Demodulate	RF Input	
3	QAM64 Demodulate	RF Input	
4	QAM256 Demodulate	RF Input	

3 Hardware support

No.	Items	Version No.	Remark
1	Version No.	Ver. 2.00	
2	BOM	Ver. 2.00	
3	Schematic	Ver. 2.00	
4	GBR	Ver. 2.00	
5	PCB	Ver. 2.00	

4 Software support

No.	Software support	Version No.	Remark
1	Driver version	NA	
2	I2C Controller	Ver. 1.0.0.80	

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