



AU85X3 Demo Board

Design Brief

1 Introduction

This design is to demo our AU8503/AU8523 chip. It supports the ATSC, J83B (AU8523 only), NTSC, PAL (only with 8MHz tuner), BTSC, IIS output and FM radio (AU8523 only).

2 Hardware Configuration

2.1 Major chip set:

No.	Function	Name/Module	Manufactory/Data sheet	Provider	Remark
1	ATSC/NTSC etc. Decoder	AU8523	Auvitek/ \zeus\marketing\data sheet\	Auvitek.	
2	Tuner	DVT-6A/W41F2	XUGUANG	XUGUANG	
3	Tuner	DTZ1W400	Qingjia	Qingjia	
4	Tuner	DA19WPS-4-E	TCL	TCL	
5	Tuner	TD1336	PHILIPS	PHILIPS	
6	Tuner	DTT7600	THOMSON	THOMSON	
7	Tuner	MT2130	Microtune	Microtune	

2.2 Functions List:

No.	Function	Information	Remark
1	Tuner Setting	Support six tuners	
2	ATSC Demodulate		
3	QAM64 Demodulate		AU8523 only
4	QAM256 Demodulate		AU8523 only
5	NTSC Demodulate		
6	PAL BG/DK/I Demodulate		AU8523 only with 8MHz tuner
7	ATV Audio output		
8	FM Radio		AU8523 only
9	BTSC input		
10	Smart Antenna		AU8523 only

3 Hardware support

No.	Items	Version No.	Remark
1	Version No.	Ver. 2.00	
2	BOM	Ver. 2.00	
3	Schematic	Ver. 2.00	
4	GBR	Ver. 2.00	
5	PCB	Ver. 2.00	

4 Software support

No.	Software support	Version No.	Remark
1	Driver version		
2	I2C Controller		